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- Nodal Analysis Stage 1:

Label Nodes

Nodal Analysis Stage 2:

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- Floating Voltage Sources
- Weighted Average Circuit
- Digital-to-Analog

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- Dependent Voltage

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The aim of nodal analysis is to determine the voltage at each node relative to the reference node (or ground). Once you have done this you can easily work out anything else you need.

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There are two ways to do this:

- (1) Nodal Analysis systematic; always works
- (2) Circuit Manipulation ad hoc; but can be less work and clearer

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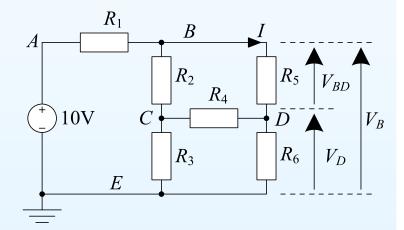
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A node is all the points in a circuit that are directly interconnected. We assume the interconnections have zero resistance so all points within a node have the same voltage. Five nodes: A, \dots, E .



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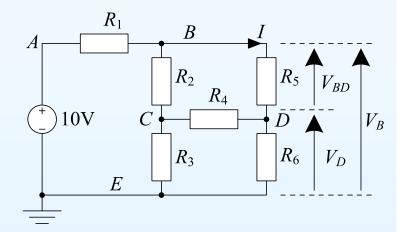
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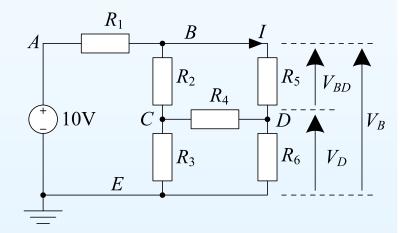
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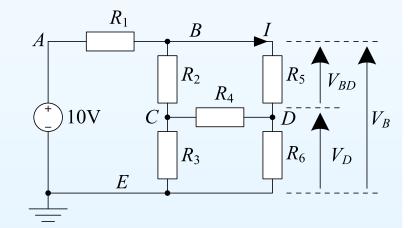
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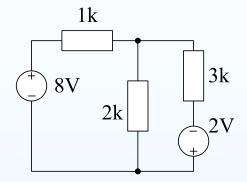
KCL: Total current exiting any closed region is zero.

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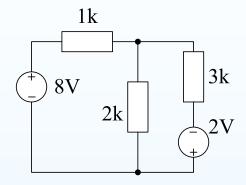
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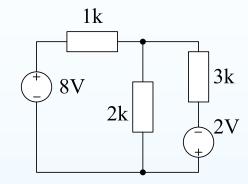
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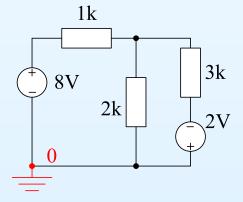
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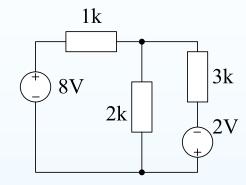


3: Nodal Analysis

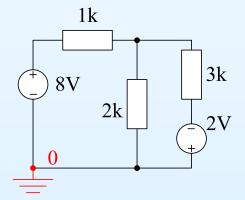
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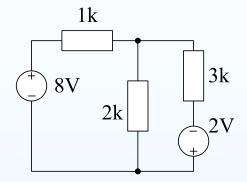


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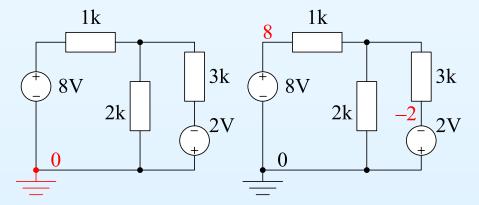
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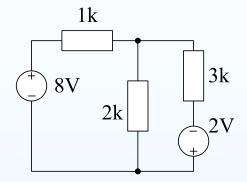
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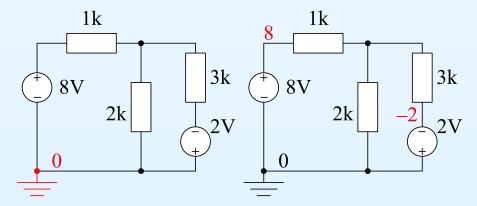
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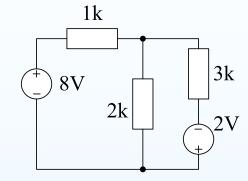


3: Nodal Analysis

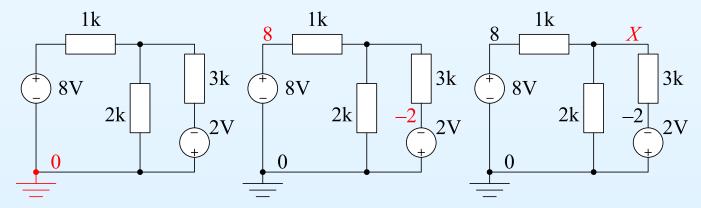
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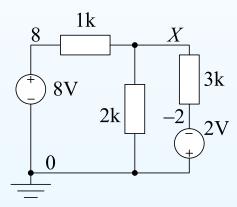
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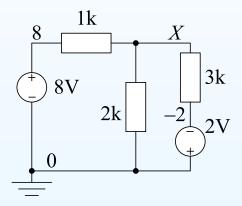
The second step is to write down a KCL equation for each node labelled with a variable by setting the total current flowing out of the node to zero. For a circuit with N nodes and S voltage sources you will have N-S-1 simultaneous equations to solve.

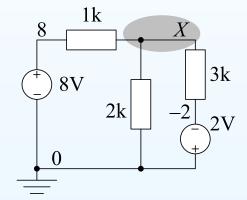


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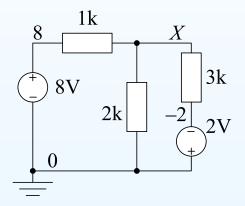
We only have one variable:

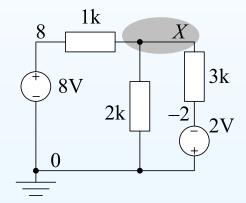
$$\frac{X-8}{1 \, \mathbf{k}} + \frac{X-0}{2 \, \mathbf{k}} + \frac{X-(-2)}{3 \, \mathbf{k}} = 0$$

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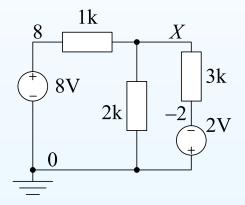
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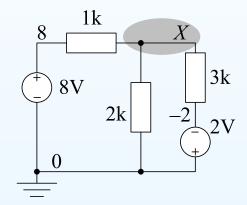
Numerator for a resistor is always of the form $X-V_N$ where V_N is the voltage on the other side of the resistor.

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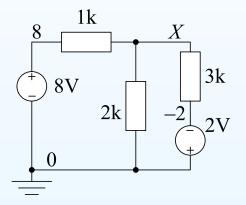
$$\frac{X-8}{1 \text{ k}} + \frac{X-0}{2 \text{ k}} + \frac{X-(-2)}{3 \text{ k}} = 0 \quad \Rightarrow \quad (6X - 48) + 3X + (2X + 4) = 0$$

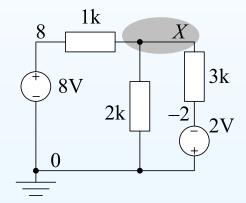
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$$11X = 44 \quad \Rightarrow \quad X = 4$$

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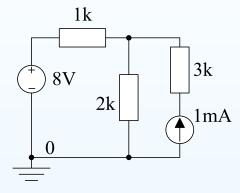
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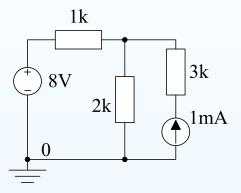
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Current sources cause no problems.

(1) Pick reference node.



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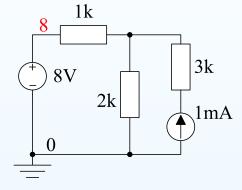
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- (1) Pick reference node.
- (2) Label nodes: 8



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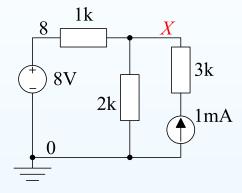
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- (1) Pick reference node.
- (2) Label nodes: 8, X



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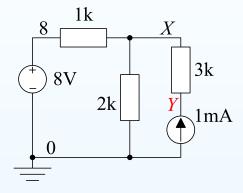
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- (1) Pick reference node.
- (2) Label nodes: 8, X and Y.



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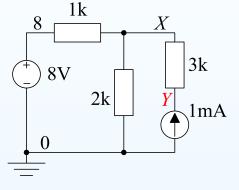
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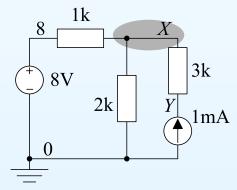
Current sources cause no problems.

- (1) Pick reference node.
- (2) Label nodes: 8, X and Y.



(3) Write equations

$$\frac{X-8}{1} + \frac{X}{2} + \frac{X-Y}{3} = 0$$



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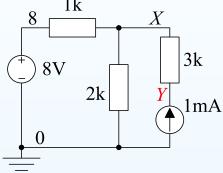
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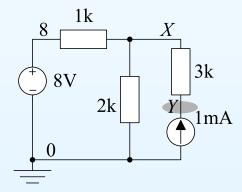


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$$\frac{X-8}{1} + \frac{X}{2} + \frac{X-Y}{3} = 0$$

$$\frac{Y - X}{3} + (-1) = 0$$



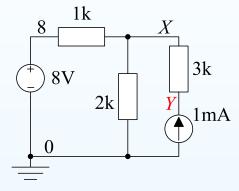


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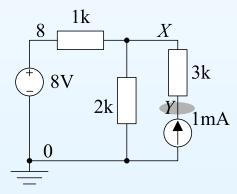
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$$\frac{Y-X}{3} + (-1) = 0$$



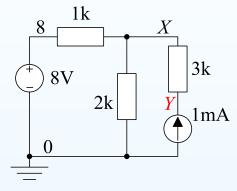
Ohm's law works OK if all resistors are in $k\Omega$ and all currents in mA.

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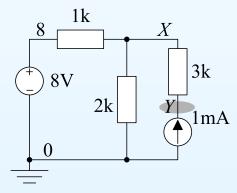
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- (2) Label nodes: 8, X and Y.



(3) Write equations

$$\frac{X-8}{1} + \frac{X}{2} + \frac{X-Y}{3} = 0$$

$$\frac{Y-X}{3} + (-1) = 0$$



Ohm's law works OK if all resistors are in $k\Omega$ and all currents in mA.

(4) Solve the equations: $X=6,\ Y=9$

3: Nodal Analysis

- Aim of Nodal Analysis
- Nodal Analysis Stage 1:

Label Nodes

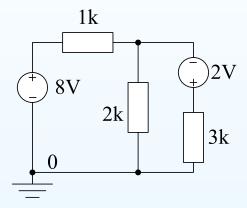
- Nodal Analysis Stage 2: KCL Equations
- Current Sources
- Floating Voltage Sources
- Weighted Average Circuit
- Digital-to-Analog

Converter

- Dependent Sources
- Dependent Voltage

Sources

- Universal Nodal Analysis Algorithm
- Summary



3: Nodal Analysis

- Aim of Nodal Analysis
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Label Nodes

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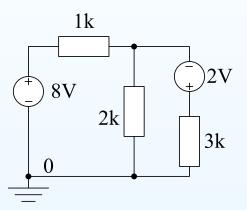
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Floating voltage sources have neither end connected to a known fixed voltage. We have to change how we form the KCL equations slightly.

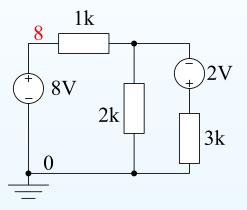
(1) Pick reference node.



3: Nodal Analysis

- Aim of Nodal Analysis
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- (1) Pick reference node.
- (2) Label nodes: 8



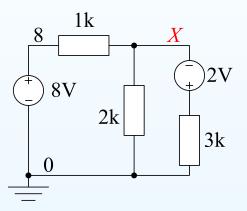
3: Nodal Analysis

- Aim of Nodal Analysis
- Nodal Analysis Stage 1:

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- (1) Pick reference node.
- (2) Label nodes: 8, X



3: Nodal Analysis

- Aim of Nodal Analysis
- Nodal Analysis Stage 1:

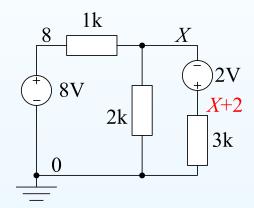
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- (1) Pick reference node.
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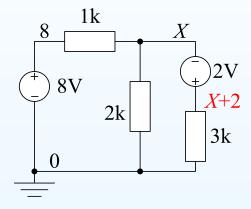


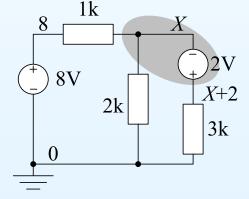
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- (1) Pick reference node.
- (2) Label nodes: 8, X and X+2 since it is joined to X via a voltage source.
- (3) Write KCL equations but count all the nodes connected via floating voltage sources as a single "super-node" giving one equation

$$\frac{X-8}{1} + \frac{X}{2} + \frac{(X+2)-0}{3} = 0$$





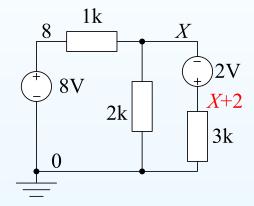
3: Nodal Analysis

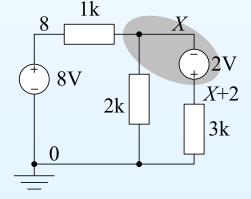
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Ohm's law always involves the difference between the voltages at either end of a resistor. (Obvious but easily forgotten)

3: Nodal Analysis

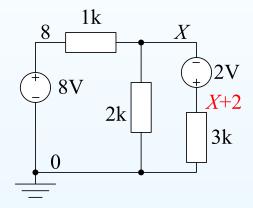
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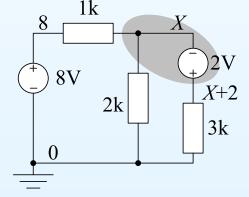
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$$\frac{X-8}{1} + \frac{X}{2} + \frac{(X+2)-0}{3} = 0$$

(4) Solve the equations: X=4





Ohm's law always involves the difference between the voltages at either end of a resistor. (Obvious but easily forgotten)

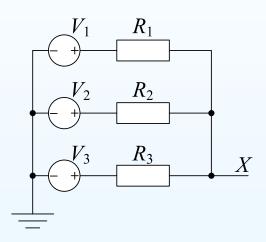
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A very useful sub-circuit that calculates the weighted average of any number of voltages.



3: Nodal Analysis

- Aim of Nodal Analysis
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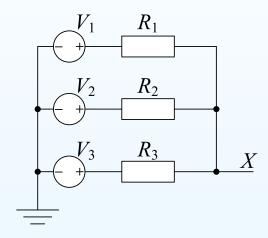
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KCL equation for node X:

$$\frac{X - V_1}{R_1} + \frac{X - V_2}{R_2} + \frac{X - V_3}{R_3} = 0$$



3: Nodal Analysis

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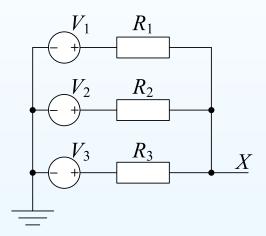
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Or using conductances:

$$(X - V_1)G_1 + (X - V_2)G_2 + (X - V_3)G_3 = 0$$

3: Nodal Analysis

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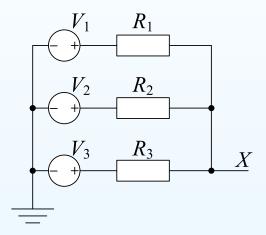
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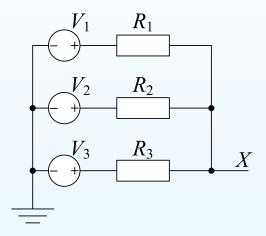
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3: Nodal Analysis

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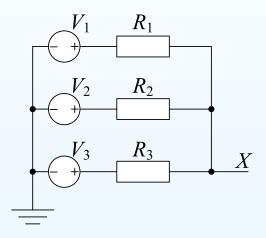
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Voltage X is the average of V_1 , V_2 , V_3 weighted by the conductances.

3: Nodal Analysis

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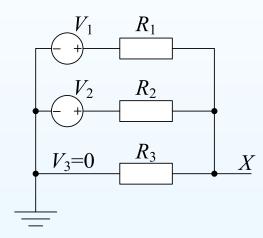
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KCL equation for node X:

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Still works if $V_3 = 0$.



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A 3-bit binary number, b, has bit-weights of 4, 2 and 1. Thus 110 has a value 6 in decimal.

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3: Nodal Analysis

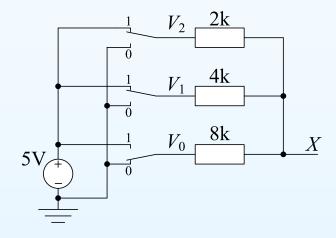
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We use $b_2b_1b_0$ to control the switches which determine whether $V_i=5~{\rm V}$ or $V_i=0~{\rm V}$. Thus $V_i=5b_i$. Switches shown for b=6.



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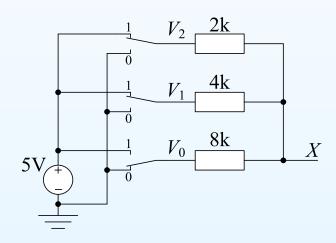
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$$X = \frac{\frac{1}{2}V_2 + \frac{1}{4}V_1 + \frac{1}{8}V_0}{\frac{1}{2} + \frac{1}{4} + \frac{1}{8}}$$



$$G_2 = \frac{1}{R_2} = \frac{1}{2k} = \frac{1}{2} \text{mS}, \dots$$

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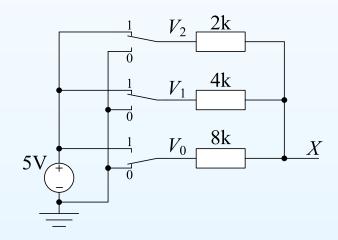
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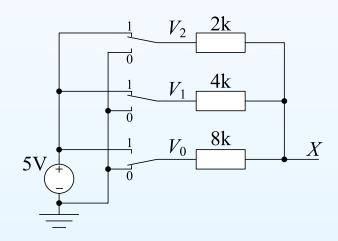
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but $V_i = 5 \times b_i$ since it connects to either $0 \, \mathrm{V}$ or $5 \, \mathrm{V}$



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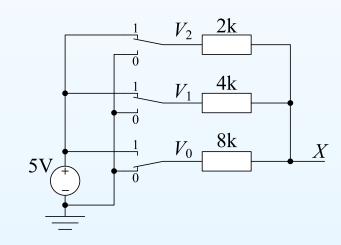
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So we have made a circuit in which X is proportional to a binary number b.

3: Nodal Analysis

- Aim of Nodal Analysis
- Nodal Analysis Stage 1:

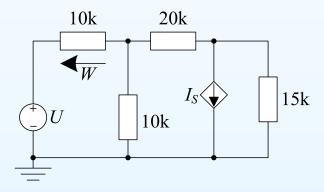
Label Nodes

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A *dependent* voltage or current source is one whose value is determined by voltages or currents elsewhere in the circuit. These are most commonly used when modelling the behaviour of transistors or op-amps. Each dependent source has a defining equation.



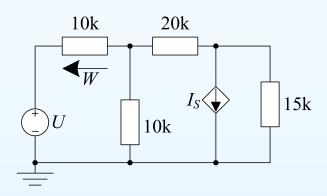
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In this circuit: $I_S=0.2W$ mA where W is in volts.

(1) Pick reference node.



3: Nodal Analysis

- Aim of Nodal Analysis
- Nodal Analysis Stage 1:

Label Nodes

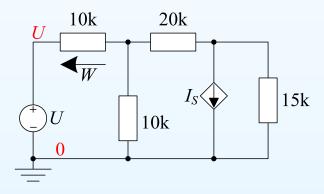
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• Dependent Sources

- Dependent Voltage Sources
- Universal Nodal Analysis Algorithm
- Summary

A *dependent* voltage or current source is one whose value is determined by voltages or currents elsewhere in the circuit. These are most commonly used when modelling the behaviour of transistors or op-amps. Each dependent source has a defining equation.

- (1) Pick reference node.
- (2) Label nodes: 0, U

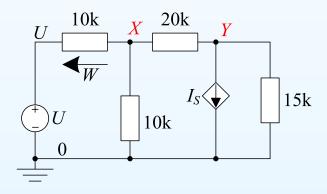


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- Nodal Analysis Stage 1:
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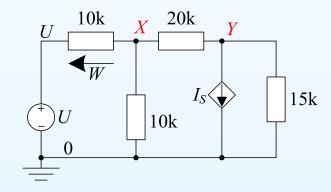
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- (1) Pick reference node.
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- (3) Write equation for the dependent source, I_S , in terms of node voltages: $I_S = 0.2 \, (U X)$



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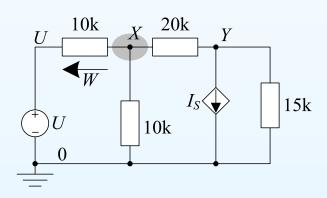
In this circuit: $I_S=0.2W$ mA where W is in volts.

- (1) Pick reference node.
- (2) Label nodes: $0,\ U, X$ and Y .
- (3) Write equation for the dependent source, I_S , in terms of node voltages:

$$I_S = 0.2 \left(U - X \right)$$

(4) Write KCL equations:

$$\frac{X-U}{10} + \frac{X}{10} + \frac{X-Y}{20} = 0$$



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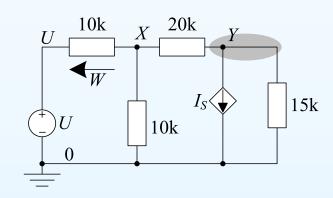
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$$\frac{X-U}{10} + \frac{X}{10} + \frac{X-Y}{20} = 0$$

$$\frac{Y - X}{20} + I_S + \frac{Y}{15} = 0$$

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(5) Solve all three equations to find X and Y in terms of U:

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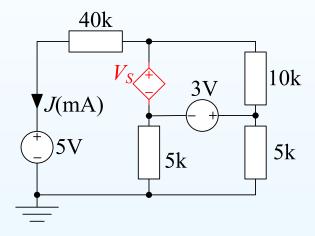
(5) Solve all three equations to find X and Y in terms of U:

$$X = 0.1U, Y = -1.5U$$

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The value of the highlighted dependent voltage source is $V_S = 10J$ Volts where J is the indicated current in mA.



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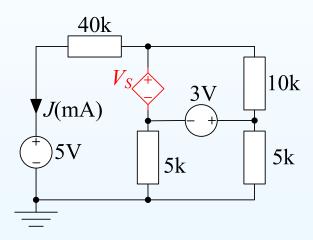
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(1) Pick reference node.



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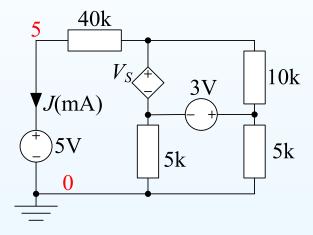
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The value of the highlighted dependent voltage source is $V_S = 10J$ Volts where J is the indicated current in mA.

- (1) Pick reference node.
- (2) Label nodes: 0, 5



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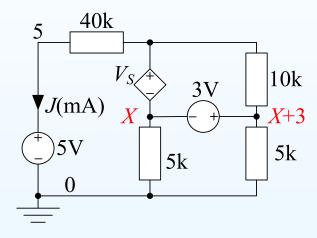
 Universal Nodal Analysis Algorithm

Summary

The value of the highlighted dependent voltage source is $V_S=10J$ Volts where J is the indicated current in mA.

(1) Pick reference node.

(2) Label nodes: 0, 5, X, X + 3



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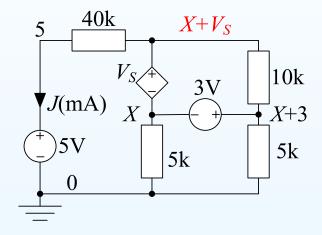
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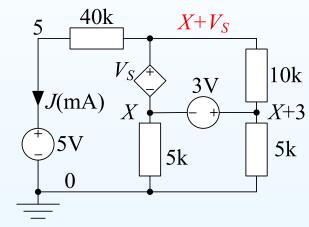


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$$V_S = 10J = 10 \times \frac{X + V_S - 5}{40} \Rightarrow 3V_S = X - 5$$

3: Nodal Analysis

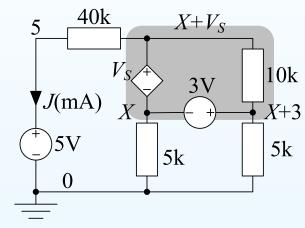
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(4) Write KCL equations: all nodes connected by floating voltage sources and all components connecting these nodes are in the same "super-node"

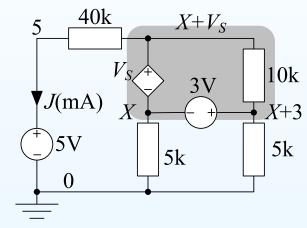
$$\frac{X + V_S - 5}{40} + \frac{X}{5} + \frac{X + 3}{5} = 0$$

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$$\frac{X + V_S - 5}{40} + \frac{X}{5} + \frac{X + 3}{5} = 0$$

(5) Solve the two equations: X=-1 and $V_S=-2$

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- (1) Pick any node as the voltage reference. Label its voltage as $0~\rm V$. Label any dependent sources with $V_S,~I_S,~\ldots$
- (2) If any voltage sources are connected to a labelled node, label their other ends by adding the value of the source onto the voltage of the labelled end.
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- Nodal Analysis
 - Simple Circuits (no floating or dependent voltage sources)

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- Simple Circuits (no floating or dependent voltage sources)
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 - use supernodes: all the nodes connected by floating voltage sources (independent or dependent)

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- Simple Circuits (no floating or dependent voltage sources)
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- Dependent Voltage and Current Sources
 - Label each source with a variable
 - Write extra equations expressing the source values in terms of node voltages
 - Write down the KCL equations as before

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- Mesh Analysis (in most textbooks)
 - Alternative to nodal analysis but doesn't work for all circuits
 - No significant benefits ⇒ ignore it

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For further details see Hayt et al. Chapter 4.